

# Use Cases: Internal



QU-PILOT

Semiconducting platform

Computing  
Sensing



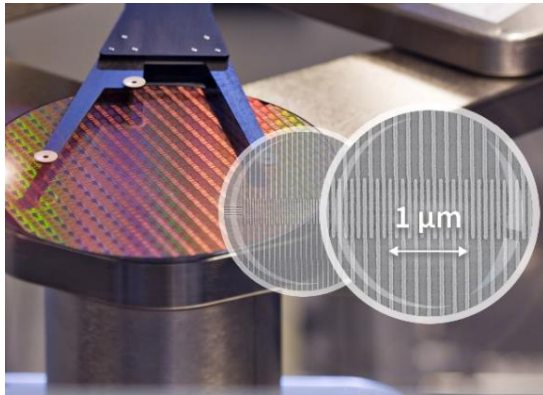
Task	Company	RTO	Use-Case
T6.8	Infineon	FhG-IPMS	Process integration for semiconductor qubits

## What & Why

- Semiconductor (quantum dot) qubit fabrication and process integration methodology
- For CMOS-style QPUs

## Task

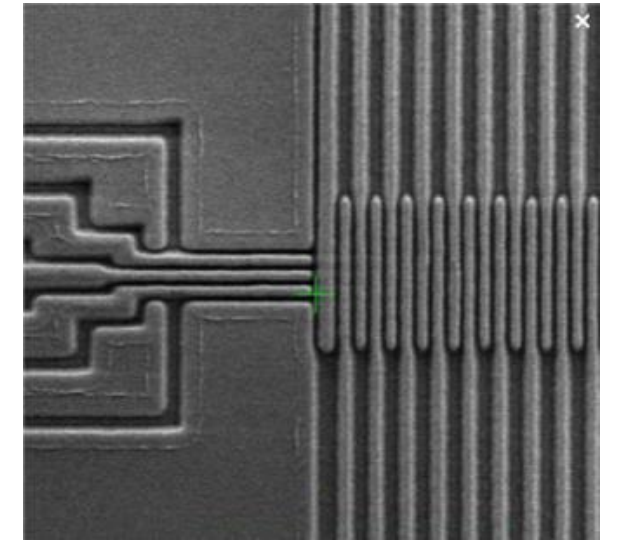
- Nanopatterning of gate-structures
- Micro magnet fabrication (CMOS compatible)
- Characterization and benchmarking



Wafer processing at Infineon



Integration scheme for micromagnets



Patterned spin qubit gate structure

